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Date:	November 7, 2006		
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From:	Jon W. Hallman	Date Sent:	
Subject:	Applicant: Agrawal et al. Serial No. 10/022,464	Time Sent:	
Client/File:	M-9642 US	Fax Operator:	

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IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant(s): Om P. Agrawal; Jinghui Zhu
Assignee: Lattice Semiconductor Corporation
Title: Block Oriented Architecture For A Programmable Interconnect Circuit
Serial No.: 10/022,464 Issue Date December 14, 2001
Examiner: Vibol Tan Group Art Unit: 2819
Docket No.: M-9642 US Confirmation No. 1715

Irvine, California
November 7, 2006

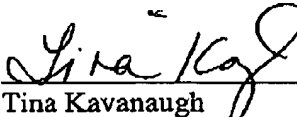
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1. Transmittal Letter (1 page)
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Dated: November 7, 2006


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-1-

Serial No. 10/022,464

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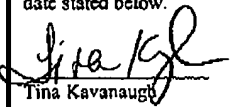
Alexandria, VA 22313-1450

Re: Applicant(s): Om P. Agrawal; Jinghui Zhu
Title: Block Oriented Architecture For A Programmable
Interconnect Circuit
Serial No.: 10/022,464 Filed: 12/14/2001
Examiner: Vibol Tan Group Art Unit: 2819
Docket No.: M-9642 US Confirmation No. 1715

Dear Sir:

Transmitted herewith are the following documents in the above-identified application:

1. Facsimile Cover Sheet (1 page)
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3. Fee Address "Indication Form" (1 page)
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Respectfully submitted,



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